

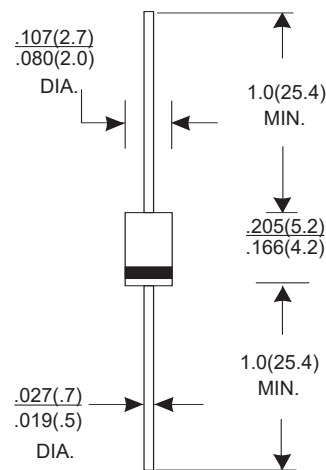
DO-41 PLASTIC SILICON RECTIFIERS

FEATURES

- High current capability
- High reliability
- High surge current capability
- High speed switching

MECHANICAL DATA

- Case:JEDEC DO--41,molded plastic
- Terminals: Axial lead ,solderable per
- MIL- STD-202,Method 208
- Polarity: Color band denotes cathode
- Mounting position: Any



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND CHARACTERISTICS

@ 25°C Ambient Temperature (unless otherwise noted)

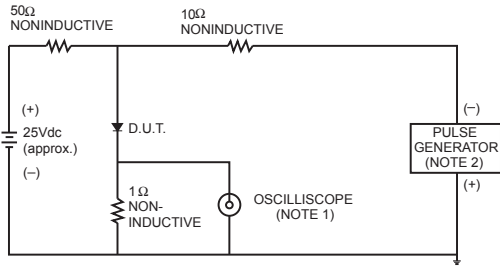
		SF11	SF12	SF13	SF14	SF15	SF16	SF17	SF18	UNITS
Maximum recurrent peak reverse voltage	V_{RRM}	50	100	150	200	300	400	500	600	V
Maximum RMS voltage	V_{RMS}	35	70	105	140	210	280	420	560	V
Maximum DC blocking voltage	V_{DC}	50	100	150	200	300	400	500	600	V
Maximum Average Forward Rectified Current, 375"(9.5mm) Lead Length at $T_A=75^{\circ}C$	$I_{F(AV)}$	1.0								A
Peak Forward Surge Current, 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	30.0								A
Maximum Instantaneous Forward Voltage at 1.0A	V_F	1.0			1.3		1.7			V
Maximum reverse current at rated DC blocking voltage	@ $T_A=25$	5.0								μA
	@ $T_A=100$	100.0								
Maximum reverse recovery time (Note1)	t_{rr}	35								ns
Typical junction capacitance (Note2)	C_J	50								pF
Typical thermal resistance(Note3)	$R_{\theta JA}$	60								$^{\circ}C/W$
Operating junction temperature range	T_j	- 55 ---- + 125								$^{\circ}C$
Storage temperature range	T_{STG}	- 55 ---- + 150								$^{\circ}C$

Note: 1.Reverse recovery condition $I_F=0.5A, I_R=1.0A, I_{rr}=0.25A$

2.Measured at 1MHz and applied reverse voltage of 4.0V D.C.

RATINGS AND CHARACTERISTIC CURVES

FIG.1- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm, 22pF.
2. Rise Time= 10ns max., Source Impedance= 50 ohms.

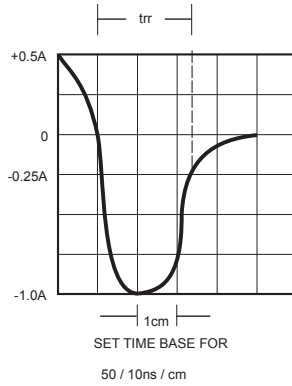


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

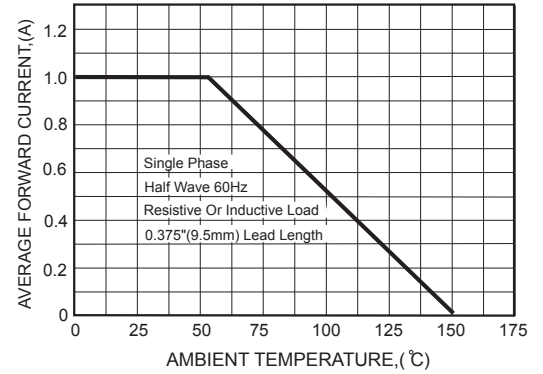


FIG.3-TYPICAL FORWARD CHARACTERISTICS

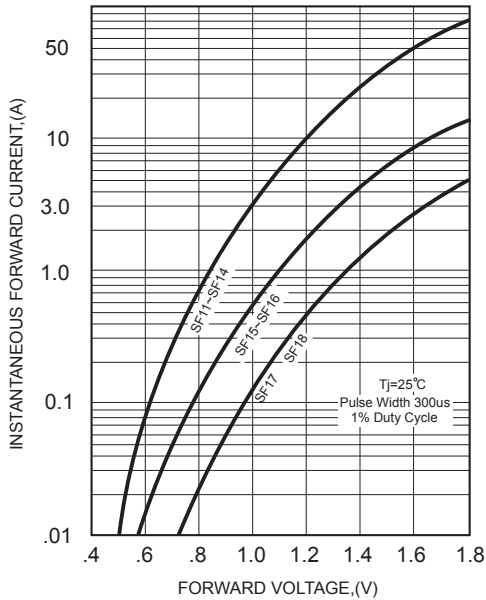


FIG.4-TYPICAL REVERSE CHARACTERISTICS

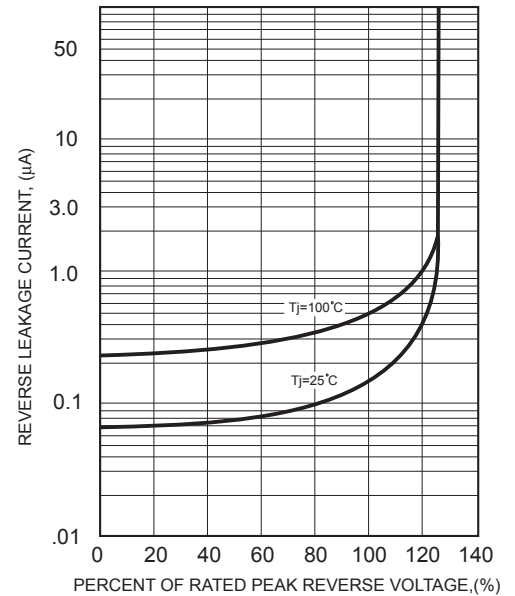


FIG.5-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

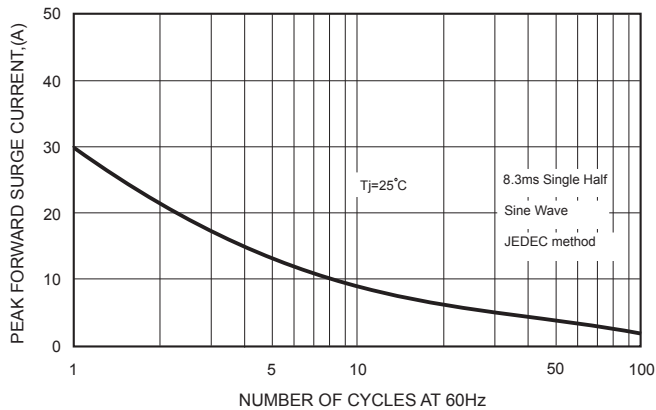


FIG.6-TYPICAL JUNCTION CAPACITANCE

